

INVENTOR INFORMATION

10/031445

IC13 Rec'd PCT/PTO 22 JAN 2002

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APPLICATION INFORMATION

Title Line One:: BIPOLAR TRANSISTOR AND FABRICATION METHO
Title Line Two:: D THEREOF
Total Drawing Sheets:: 13
Formal Drawings?: Yes
Application Type:: Utility
Docket Number:: 740819-734
Secrecy Order in Parent Appl.?: No

REPRESENTATIVE INFORMATION

Representative Customer Number:: 22204
Registration Number One:: 38285
Registration Number Two:: 28290
Registration Number Three:: 35483

Registration Number Four:: 45285
Registration Number Five:: 26477

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2013 EPOCH PCT/PTC 22 JAN 2002

CONTINUITY INFORMATION

This application is a:: 371 OF
> Application One:: PCT/JP01/04344
Filing Date:: 05-23-2001

PRIOR FOREIGN APPLICATIONS

Foreign Application One:: 2000-151044
Filing Date:: 05-23-2000
Country:: Japan
Priority Claimed:: Yes

Source:: PrintEFS Version 1.0.1